

# STEPPING UP SIRIUS - TIPS AND TRICKS FOR YOUR VISUALIZATION JOURNEY!

# PLAT4MC Visualization tool

C Code → SCA2AMALTHEA → AMALTHEA Model → PLAT4MC Visualization  
 ELF/MAP → ELF2AMALTHEA → AMALTHEA Model → PLAT4MC Visualization

Visualize - Analyze - Optimize

## System Analysis / Architecture View

Verify and visualize communications between components, labels and runnables based on constraints you choose.

### Task Interaction

Visualize interaction between tasks

### Label Dependency

Visualize label is accesses

### Runnable Access

Visualize label accesses / runnable calls made by the runnable

### Task Composition

Visualize contents within the task

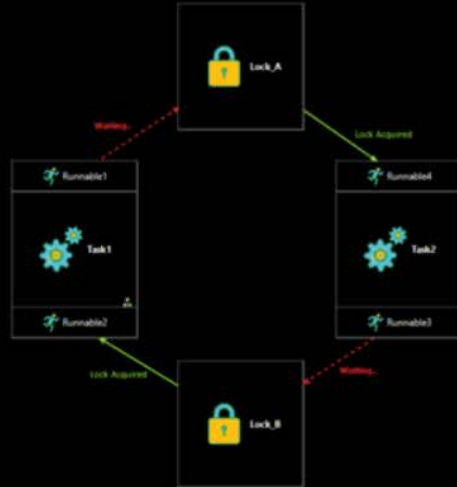
COMPONENT NAME/LS	COMP 1	COMP 2	COMP 3	COMP 4
COMP 1		X		
COMP 2	X		X	X
COMP 3		X		
COMP 4		X		

A red cross in the table indicates an illegal Communication between components. It can be also be visualized by the red coloured read/write line in the diagram.

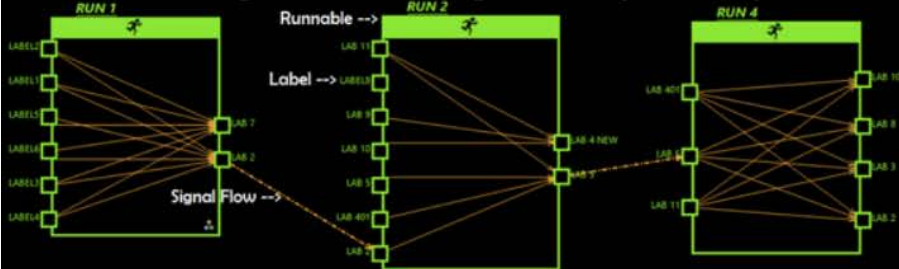
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## In-Depth System Analysis

### Detect and visualize Deadlocks



### Visualize Signal Flow from its origin all the way to the endpoint



## Load Visualization

Visualize core/bus load before and after using *Locomo* to optimize distribution of variables and functions in  $\mu$ C memories.

